

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Vivek Nautiyal, et al.

Serial No.:

10/768,962

Filed:

January 30, 2004

For:

METHOD AND SYSTEM FOR REDUCING POWER CONSUMPTION IN DIGITAL CIRCUITRY USING

CHARGE REDISTRIBUTION CIRCUITS

Group No.:

2816

Examiner:

Jeffery S. Zweizig

MAIL STOP RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

In response to the Office Action dated May 25, 2005, the Applicants respectfully request continued examination of the above-identified patent application.

Please amend the above-identified patent application as follows.